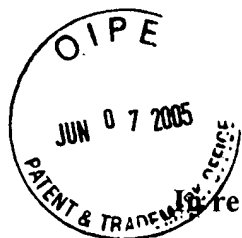


JPW



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inter Application of:

Cobbley et al.

Serial No.: 10/721,110

Filed: November 24, 2003

For: METHOD FOR IN-LINE TESTING
OF FLIP-CHIP SEMICONDUCTOR
ASSEMBLIES

Confirmation No.: 6106

Examiner: R. Kobert

Group Art Unit: 2829

Attorney Docket No.: 2269-3437.9US
97-0514.09/US

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

June 3, 2005
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Leta M. Howard
Name (Type/Print)

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the document listed on attached Form PTO-1449 or PTO/SB/08 be considered by the Examiner and made of record. Copies of U.S. patents and pending patents are not being submitted pursuant to M.P.E.P. 609 III A(2). The listed document was cited by the Office in co-pending application Serial No. 10/900,610, filed on July 27, 2005, and directed to a related invention.

Serial No. 10/721,110

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

U.S. Patent Documents

<u>U.S. Patent No.</u>	<u>Publication Date</u>	<u>Patentee</u>
US - 5,532,612	July 2, 1996	Liang

Applicants offer to supply any explanation or discussion of the document which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is believed to be filed before the mailing date of the first Office Action on the merits subsequent to the filing of an RCE in the above-identified application.

Respectfully submitted,



Kevin K. Johanson
Registration No. 38,506
Attorney for Applicant(s)
TRASKBRITT
P.O. Box 2550
Salt Lake City, Utah 84110-2550
Telephone: 801-532-1922

Date: June 3, 2005
KKJ/nj:lmh

Enclosures: Form PTO-1449 or PTO/SB/08

Document in ProLaw

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet	1	of	1
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Complete if Known

Application Number	10/721,110
Filing Date	November 24, 2003
First Named Inventor	Cobbley et al.
Group Art Unit	2829
Examiner Name	R. Kobert
Attorney Docket Number	2269-3437.9US (97-0514.09/US)

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]Examiner
SignatureDate
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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